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 U.S. 09/892713
 06/28/01

APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/783,023	02/15/2001	2812	710	01701.00059	14	20	2

RECEIVED**CONFIRMATION NO. 6061****FILING RECEIPT****MAR 21 2001***JL-324*

OC000000005867675
BANNER & WITCOFF LTD.

Date Mailed: 03/15/2001

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Applicant(s)

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 Yusuke Kohyama, Yokosuka-shi, JAPAN;

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GPO
Docketed

Continuing Data as Claimed by Applicant

MAR 22 2001

Foreign Applications

JAPAN 2000-039968 02/17/2000

If Required, Foreign Filing License Granted 03/15/2001**Projected Publication Date:** 08/23/2001**Non-Publication Request:** No**Early Publication Request:** No**Title**

Semiconductor device using fuse/anti-fuse system and method of manufacturing the same

Preliminary Class

438

Data entry by : PETROS, TADELECH

Team : OIPE

Date: 03/15/2001

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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PATENT DESIGN B&W Ref. 01701.00059 Date 2/15/01
 HAND CARRY Group/Section Bldg _____ Rm _____
 Serial/Patent No. TBA Atty/Sec JMP
 Inventor YOSHIAKI FUKUZUMI ET AL Client SUZUYE & SUZUYE
 Title SEMICONDUCTOR DEVICE USING FUSE / ANTI-FUSE
SYSTEM AND METHOD OF MANUFACTURING THE SAME
 The following has been received in the U.S. Patent and Trademark Office on the date stamped hereon:
 42 total pp Spec., including : # of Claims 20
 (# of independent claims 2); Abstract
 Drawings : Formal Informal 33
 # of distinct sheets 14 : Figs. 33
 Declaration/PoA : Executed Unexecuted
 Assignment w/PTO Cover Sheet
 Small Entity Declaration
 IDS w/PTO 1449 References w/Fee
 Preliminary Amendment
 Priority Claim (Foreign or U.S. Prov. B&W# 1449 2000-034968 2/17/00)
 Country Appl. # Date
 w/Foreign Priority Document(s)
 Application : CIP Continuation Divisional
 Parent Ser. No. B&W#
 U.S. Provisional pp Spec/Cmts; Cover Sht.
 Response to Missing Parts/Rqmts. dtd
 Request for Expedited Foreign Filing License
 Request for Corrected : Filing Rec Assignment
 Response to Restriction/Election Requirement

Sequence Listing : Diskette Paper
 Amendment Response : OA dtd
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RETURN WHEN SERIAL NUMBER IS ASSIGNED TO APPLICATION

USPTO: Please return this card, indicating receipt date and Application Serial
No. of the following patent application:

Applicant(s): YOSHIAKI FUKUZUMI ET AL

Title: SEMICONDUCTOR DEVICE USING FUSE /
ANTI-FUSE SYSTEM AND METHOD
OF MANUFACTURING THE SAME

Filing Date: 2/15/01

Client: SUZUYE & SUZUYE

Attorney/Secretary: JMP

B&W Rev 1000



Clicit Ref.: T1KK-0051437